

WS4695C

28V, Over-Voltage, Over-Current Protection Load Switch with Adjustable Current-Limit Control

Descriptions

The WS4695C is a small, low RON, single channel load switch with controlled slew rate. The device can operate over an input voltage range of 2.6 V to 5.5 V and can set current limit from 0.1A to 3.5A.

The controlled rise time of the device greatly reduces inrush current caused by large bulk load capacitance, thereby reducing or eliminating power supply droop.The WS4695C has a True Reverse-Current Blocking (TRCB) function that obstructs unwanted reverse current from VOUT to VIN during ON and OFF states. The small size and low R_{ON} makes the device ideal for being used in space constrained, battery powered applications.The wide input voltage range of the switch makes it a versatile solution for many different voltage rails.

The WS4695C are available in CSP-12L package. Standard products are Pb-free and Halogen-free.

Features

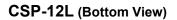
- Input Voltage Range :2.6V~5.5V
- Absolute Rating at Vout :28V
- Maximum Output current :4A
- Adjustable Current Limit :0.1A~3.5A
 2A ~3.5A with 20% Accuracy
- True Reverse-Current Blocking (TRCB)
- Under-Voltage Lockout&Thermal Shutdown
- CSP-12L

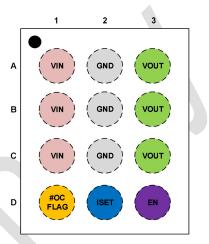
Applications

- Smart Phones, Tablet PCs
- Storage, DSLR, and Portable Devices

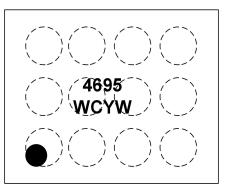
Http//:www.omnivision-group.com







Pin Configuration (Top View)

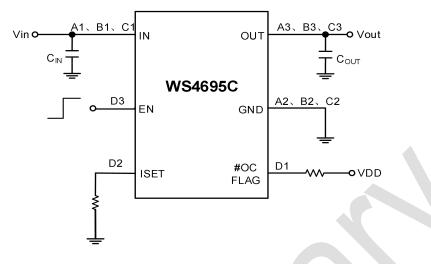


Marking (Top View) 4695:Device Code WC:Special Code Y :Year Code W:Week Code Order information

	Device	Marking Package		Shipping	
es	WS4695C-12/TR	4695	CSD 10	2000/Deel®Tene	
		WCYW	CSP-12L	CSP-12L 3000/Ree	3000/Reel&Tape



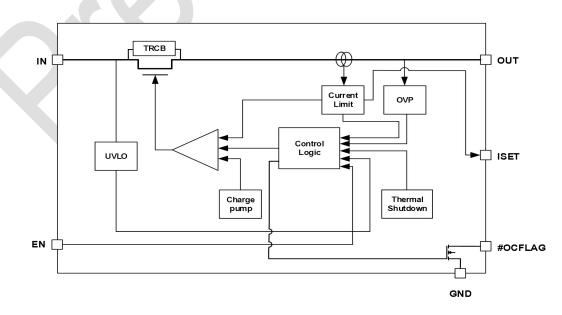
Typical Application



Pin Description

Pin Number	Symbol	Description
A3、B3、C3	OUT	Output pin
A1、B1、C1	IN	Input pin
A2、B2、C2	GND	Ground
D3	EN	ON/OFF Control Input: Active HIGH
D2	ISET	Current Limit Set Input: A resistor from ISET to ground sets the
DZ	ISET	current limit for the switch.
D1		Fault Output: Active LOW, open-drain output that indicates an input
D1	#OCFLAG	over current. External pull-up resistor to VDD is required.

Block Diagram





Absolute Maximum Ratings

These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Parame	ter	Symbol	Min.	Max.	Unit
V _{OUT} to (GND,V _{OUT} to VIN	OUT	-0.3	28.0	V
Other Pins to GND		EN,IN,ISET, #OCFLAG	-0.3	6.0	V
Maximu	m Continuous Switch Current (1)	Isw	4	.0	A
Operating Junction Temperature		TJ	-40	150	°C
Storage Temperature Range		T _{STG}	-65	150	°C
Lead Temperature		Τι	260		°C
Human Body Model, JESD22-A114		НВМ	±2000		V
ESD	Charged Device Model, JESD22-C101	CDM	±1000		V
	Air Discharge(VIN,Vout to GND)	Air Discharge	±15000		V
	Contact Discharge(VIN, VOUT to GND)	Contact Discharge	±8000		V

Notes:

(1) maximum Junction Temperature = 85°C

Recommend Operating Ratings

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications.

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	VIN	2.6	5.5	V
Other Pins	EN,IN,ISET,#OCFLAG	2.5	5.5	V
Operating ambient temperature	T _A	-40	85	°C
Thermal Resistance, ReJA (CSP-12L) (2)	R _{θJA}	88		°C/W

Notes:

(2) :Surface mounted on FR-4 Board using 2 oz, 1 square inch Cu area, PCB board size 1.5*1.5 square inches.



ELECTRICAL CHARACTERISTICS

(VIN=5V, $C_{IN} = 1\mu F$, $C_{OUT} = 1\mu F$, at Ta=25°C, unless otherwise noted)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	
Basic Operation							
Input Voltage	V _{IN}		2.6		5.5	V	
Quiescent Current	lq	V_{IN} = V_{EN} , V_{OUT} =Open		80	205	μA	
Shutdown Current	I _{SD}	V _{IN} =5.5V, V _{OUT} =0V, V _{EN} =GND		0.1	4	μA	
Off Supply Current	I _{Q(OFF)}	V _{EN} =GND, V _{OUT} =Open		1	2	μA	
	_	V _{IN} =V _{EN} =5.0V, I _{OUT} =1A		40	67	- mΩ	
On Resistance	Ron	V _{IN} =V _{EN} =3.7V, I _{OUT} =1A		44	72		
EN Logic High Voltage	VIH	V _{IN} =2.5V to 5.5V,I _{OUT} =0.1A	1.1			V	
EN Logic Low Voltage	VIL	VIN=2.5V to 5.5V, IOUT=0.1A			0.4	V	
#OCFLAG Output Logic		V _{IN} =5 V,I _{SINK} =10mA		0.1	0.2	V	
Low Voltage	VIL_FLAG	V _{IN} =2.5V,I _{SINK} =10mA		0.15	0.3	V	
#OCFLAG Output Logic High Leakage Current	I _{FLAG_LK}	V_{IN} =5V, Switch on		0.1	1	μA	
EN Input Leakage	I _{ON}	V _{EN} =0V to 5V			1	μA	
Pull-Down Resistance at EN Pin		V _{IN} =2.5~5.5V, V _{EN} =V _{IN}					
		TA= -40 to 85 °C		14		ΜΩ	
Over-Voltage Protection	1			11		1	
	.,	VOUT Rising Threshold	5.5	5.8	6		
Output OVP Lockout	Vov_trip	Vout Falling Threshold		5.5		- V	
Output OVP Hysteresis	OUT _{HYS}			0.3		V	
		I _{OUT} =0.5A, C _L =1μF, TA=25°C,			_		
OVP Response Time	tove	V _{OUT} from 5.5 V to 6.0 V	1		4	μs	
Over-Current Protection				1		1	
0		$V_{IN}=V_{EN}=5V$, RSET=300 Ω	2975	3500	4000		
Current Limit		$V_{IN}=V_{EN}=5V$, RSET=500 Ω	1768	2080	2390	mA	
		V _{IN} Increasing		2.4			
Under-Voltage Lockout	VUVLO	V _{IN} Decreasing				V	
UVLO Hysteresis	Vuvlo_hy s			200		mV	
RCB Protection Trip Point	V _{T_RCB}	V _{OUT} - V _{IN}		50		m۷	
RCB Protection Release Trip Point	V _{R_RCB}	Vin - Vout		50		mV	
RCB Hysteresis	V _{RCB_HYS}			100		mV	

ELECTRICAL CHARACTERISTICS (continued)

(VIN=5V, C_{IN} =1µF ,C_{OUT}=1µF, at Ta=25°C, unless otherwise noted)

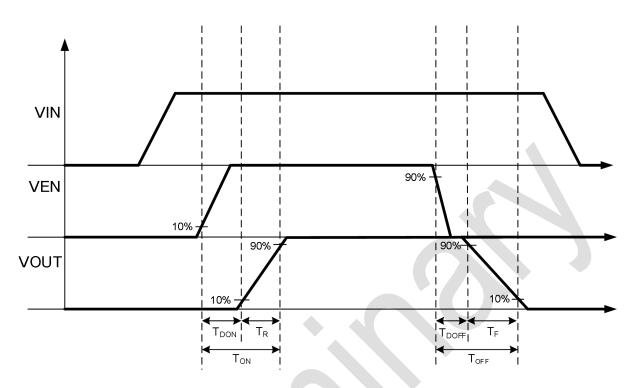
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Default RCB Response Time ⁽³⁾	t _{RCB}	V_{IN} =5V, V_{EN} =High/Low		2		μs
RCB Current	I _{RCB}	V _{EN} =0V, V _{OUT} =5.5V		7		μA
Over-Current Response Time ⁽³⁾	t _{OCP}	$\begin{array}{l} \mbox{Moderate Over-Current} \\ \mbox{Condition}, I_{OUT} \geq I_{LIM} \ , \ V_{OUT} \\ \leq V_{IN} \end{array}$		10		μs
Over-Current Flag Response Time	toc_flag	When Over-Current Occurs to Flag Pulling LOW		9		ms
Thermal Shutdown	TSD	Shutdown Threshold Return from Shutdown Hysteresis		150 130 20		°C
Turn-On Delay	T _{DON}			1.1		
V _{OUT} Rise Time	T _R			0.6		ms
Turn-On Time	T _{ON}	$V_{IN}=5V$, $R_L=100\Omega$, $C_L=1uF$		1.7		
Turn-Off Delay	TDOFF	R _{SET} =2kΩ,T _A =25°C		10		
V _{OUT} Fall Time T _F				240		μs
Turn-Off Time	TOFF			250		

Notes:

(3) This parameter is guaranteed by design.

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Timing Diagram



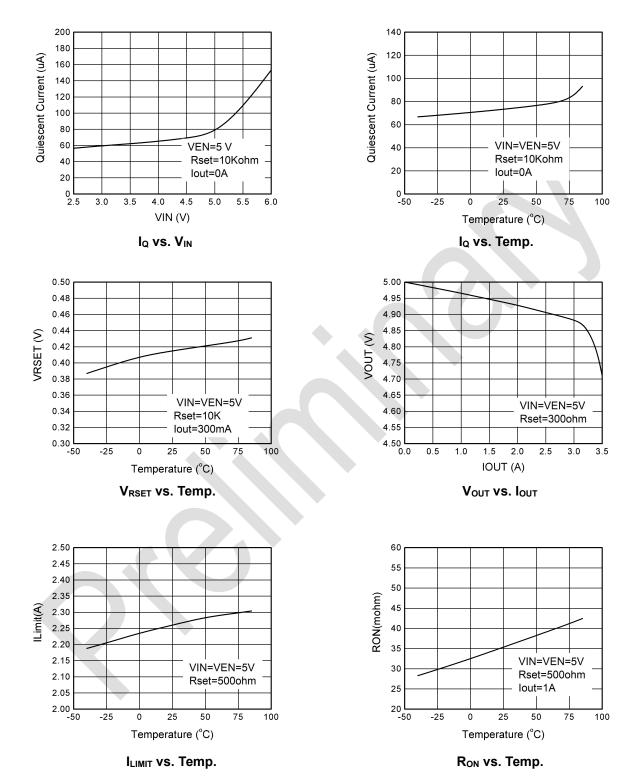
Note: $T_{ON}=T_{DON}+T_R$, $T_{OFF}=T_{DOFF}+T_{F.}$





Typical characteristics

(Ta=25°C,V_{IN}=V_{EN}=5V,C_{IN}=1µF,C_{OUT}=1µF, unless otherwise noted)





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CIN=COUT=1uF,VIN=VEN=5V,RSET=500Ω,IOUT=0mA

CIN=COUT=1uF,VIN=VEN=5V,RSET=500Ω,IOUT=0mA

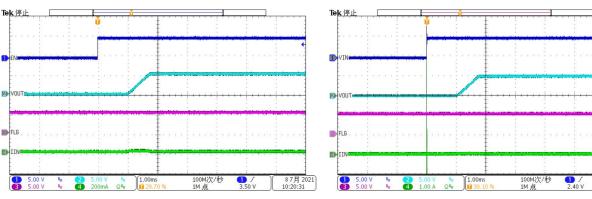
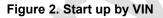


Figure 1. Start up by EN



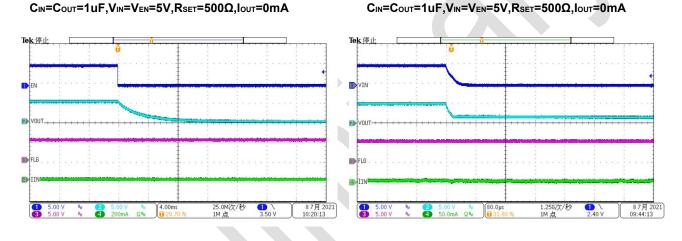
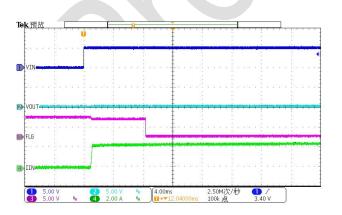


Figure 3. Shut down by EN

CIN=10uF, COUT=1uF, VIN=VEN=5V, RSET=500Q, VOUT=GND



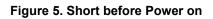


Figure 4. Shut down by EN

CIN=10uF, COUT=1uF, VIN=VEN=5V, RSET=500Q, VOUT=GND

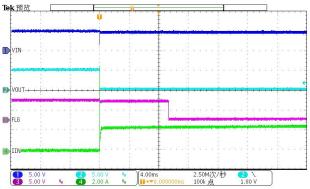


Figure 6. Short after Power on



Detailed Description

Input Capacitor

To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns on into a discharged load capacitor or short-circuit, a capacitor needs to be placed between VIN and GND. A 1- μ F ceramic capacitor, CIN, placed close to the pins, is usually sufficient. Higher values of CIN can be used to further reduce the voltage drop during high-current application.

Output capacitor

An output capacitor should be placed between the VOUT and GND pins. This capacitor prevents parasitic board inductance from forcing VOUT below GND when the switch is on. This capacitor also prevents reverse inrush current from creating a voltage spike that could damage the device in the case of a VOUT short.

Fault Reporting

Upon the detection of an over-current, #OC_FLA G signal the fault by activating LOW.

Current Limiting

The current limit ensures that the current through the switch does not exceed the maximum set value, while not limiting the minimum value. The current at which the part's limit is adjustable through the selection of the external resistor connected to the ISET pin. Information for selecting the resistor is found in the section below. The device acts as a constant-current source when the load draws more than the maximum value set by the device until thermal shutdown occurs. The device recovers if the die temperature drops below the threshold temperature.

Under-Voltage Lockout (UVLO)

The under-voltage lockout turns the switch off if the input voltage drops below the lockout threshold. With the EN pin active, the input voltage rising above the UVLO threshold releases the lockout and enables the switch.

True Reverse-Current Blocking

The true reverse-current blocking feature protects the input source against current flow from output to input regardless of whether the load switch is on or off.

Thermal Shutdown

The thermal shutdown protects the die from internally or externally generated excessive temperature. During an over-temperature condition, the switch is turned off. The switch automatically turns on again if the temperature of the die drops below the threshold temperature.

Setting Current Limit

The current limit is set with an external resistor connected between the ISET and GND pins. It can be calculated as:

$$I_{LIMIT}(A) = \frac{1000}{Rset(\Omega)}$$

The resistor tolerance of 1% or less is recommended.

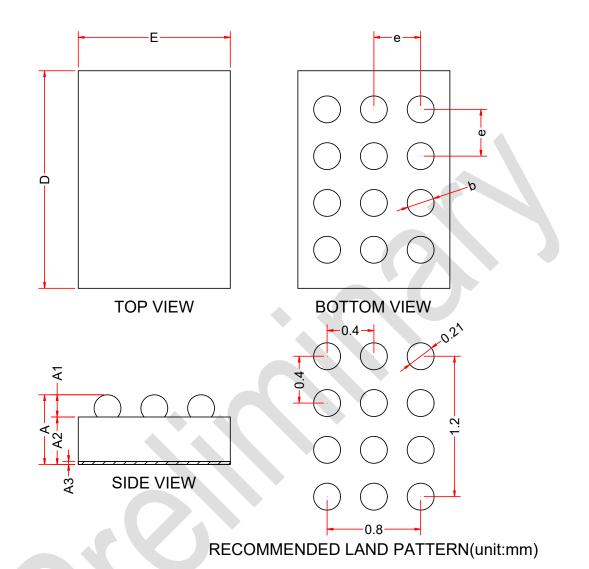
Layout guide

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effect that parasitic trace inductance may have on normal and short-circuit operation. Using wide traces for VIN, VOUT, GND helps minimize parasitic electrical effects along with minimizing the case-to ambient thermal impedance.



PACKAGE OUTLINE DIMENSIONS



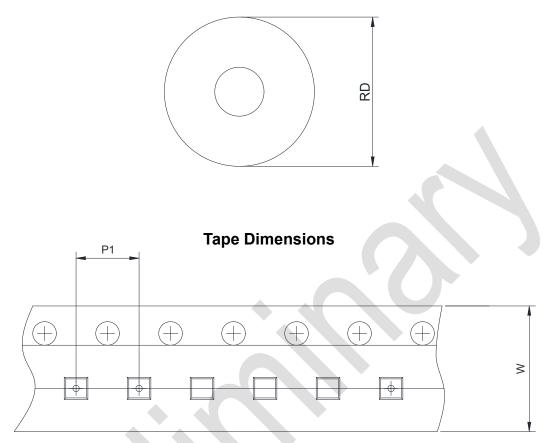


Ormahad	Dimensions in Millimeters					
Symbol	Min.	Тур.	Max.			
A	0.45	0.50	0.54			
A1	0.18	0.20	0.22			
A2	0.27	0.30	0.32			
A3		0.025REF				
D	1.80	1.83	1.86			
E	1.30	1.33	1.36			
b	0.24	0.26	0.28			
е	0.40BSC					

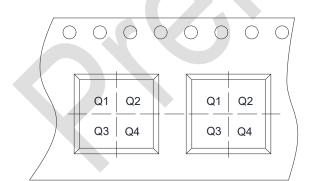


TAPE AND REEL INFORMATION





Quadrant Assignments For PIN1 Orientation In Tape





User Direction of Feed

RD	Reel Dimension	🗹 7inch	🔲 13inch		
W	Overall width of the carrier tape	🗹 8mm	🔲 12mm	🔲 16mm	
P1	Pitch between successive cavity centers	🗖 2mm	🔽 4mm	🔲 8mm	
Pin1	Pin1 Quadrant	Q 1	🗖 Q2	🗖 Q3	🗖 Q4